TOSHIBA PHOTOCOUPLER GaAs IRED & PHOTO-TRIAC

TLP3021(S),TLP3022(S),TLP3023(S)

OFFICE MACHINE HOUSEHOLD USE EQUIPMENT TRIAC DRIVER SOLID STATE RELAY

The TOSHIBA TLP3021 (S), TLP3022 (S) and TLP3023 (S) consist of photo-triac optically coupled to a gallium arsenide infrared emitting diode in a six lead plastic DIP.

- Peak Off-State Voltage : 400 V (min)
- Trigger LED Current
- **On-State** Current
 - Isolation Voltage : 5000Vrms(Min)
- UL Recognized
- SEMKO Approved

- - : 15 mA (max) (TLP3021(S)) 10 mA (max) (TLP3022(S)) 5 mA (max) (TLP3023(S))
- : 100 mA (max)

- **BSI** Approved
- : UL1577, File No.E67349 : SS EN60065
- SS EN60950, File No.9841105 : BS EN60065, File No.8385
- BS EN60950, File No.8386
- Option (D4) type

VDE approved: DIN EN60747-5-2

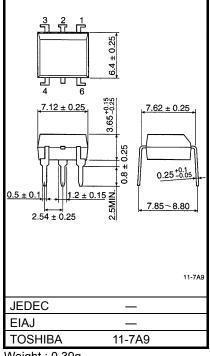
Approved No. 40009302

Maximum operating insulation voltage: 890VPK Highest permissible over voltage: 8000VPK

(Note):When a EN60747-5-2 approved type is needed, please designate the "Option (D4)"

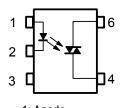
Construction Mechanical Rating

	7.62 mm pich Standard Type	10.16 mm pich TLPxxxxF Type		
Creepage Distance	7.0 mm (Min)	8.0 mm (Min)		
Clearance	7.0 mm (Min)	8.0 mm (Min)		
Insulation Thickness	0.5 mm (Min)	0.5 mm (Min)		



Weight : 0.39g

Pin Configuration (top view)



1: Anode 2: Csthode 3: N.C. 4:Terminal 1 6:Terminal 2 Unit: mm

Absolute Maximum Ratings (Ta=25°C)

	CHARACTERISTIC	SYMBOL	RATING	UNIT		
D	Forward Current	١ _F	50	mA		
	Forward Current Derating (Ta≥53°C)	∆l _F /°C	-0.7	mA /°C		
	Peak Forward Current (100µs pulse, 100pps)		I _{FP}	1	А	
LED	Power Dissipation		PD	100	mW	
	Power Dissipation Derating (Ta≥25°C)		$\Delta P_D / C$	-1.0	mW/°C	
	Reverse Voltage			5	V	
	Junction Temperature	Tj	125	°C		
	Off-State Output Terminal Voltage	V _{DRM}	400	V		
	On-State RMS Current	Ta=25°C	I	100	mA	
		Ta=70°C	I _{T(RMS)}	50		
ror	On-State Current Derating (Ta≥25°C)	∆I _T /°C	-1.1	mA /°C		
DETECTOR	Peak On-State Current (100µs pulse, 120pps)	I _{TP}	2	А		
DEJ	Peak Nonrepetitive Surge Current (Pw=10ms,DC=10	I _{TSM}	1.2	А		
	Power Dissipation	PD	300	mW		
	Power Dissipation Derating (Ta≥25°C)	$\Delta P_D / C$	-4.0	mW/°C		
	Junction Temperature	Tj	115	°C		
Stor	age Temperature Range	T _{stg}	-55~150	°C		
Ope	rating Temperature Range	T _{opr}	-40~100	°C		
Lea	d Soldering Temperature (10s)	T _{sol}	260	°C		
Tota	I Package Power Dissipation	PT	330	mW		
Tota	Total Package Power Dissipation Derating (Ta≥25°C)			-4.4	mW /°C	
Isola	Isolation Voltage (AC,1min. , R.H.≤60%) (Note 2)			5000	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 2) Device considered a two terminal device :Pins1,2 and 3 shorted together and pin6 shorted together.

Recommended Operating Conditions

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Voltage	V _{AC}	_		120	V_{ac}
Forward Current	١ _F	15	20	25	mA
Peak On-State Current	I _{TP}	_	_	1	А
Operating Temperature	T _{opr}	-25		85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

*In The case of TLP3022

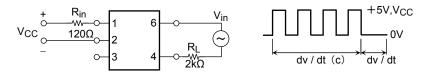
Individual Electrical Characteristics (Ta=25°C)

	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	. TYP.	MAX.	UNIT
	Forward Voltage	VF	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse Current	I _R	V _R = 5 V	_	—	10	μA
	Capacitance	CT	V = 0, f=1MHz		10	_	pF
۲	Peak Off-State Current	I _{DRM}	V _{DRM} =400V	_	10	1000	nA
0 1	Peak On-State Voltage	V _{TM}	I _{TM} =100mA	_	1.7	3.0	V
Ö	Holding Current	lΗ	_	_	0.6	_	mA
ш Н	Critical Rate of Rise of Off-State Voltage	dv/dt	Vin=120Vrms , Ta=85°C (Fig.	1) 200	500	_	V/µs
DE	Critical Rate of Rise of Commutating Voltage	dv/dt(c)	Vin=30Vrms , IT=15mA (Fig.	1) —	0.2	_	V/µs

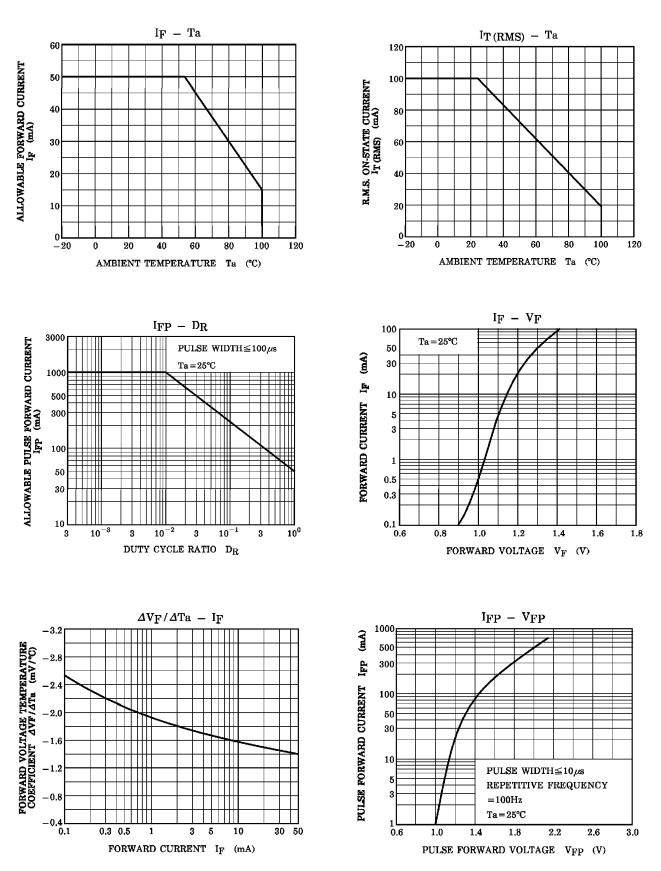
Coupled Electrical Characteristics (Ta=25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
	TLP3021(S)	I _{FT}	V _T =3V	_	_	15	mA
Trigger LED Current	TLP3022(S)			_	5	10	
	TLP3023(S)			_	_	5	
Capacitance (Input to C	Output)	C _S VS=0 , f=1MHz - 0.8 -		_	pF		
Isolation Resistance		Rs	VS=500V(R.H.≤60%)	5×10 ¹⁰	10 ¹⁴	_	Ω
			AC , 1minute	5000	—	_	Vrms
Isolation Voltage		BV_S	AC , 1second,in oil	_	10000	_	VIIIS
			DC , 1minute,in oil		10000		Vdc

Fig. 1 dv / dt test circuit

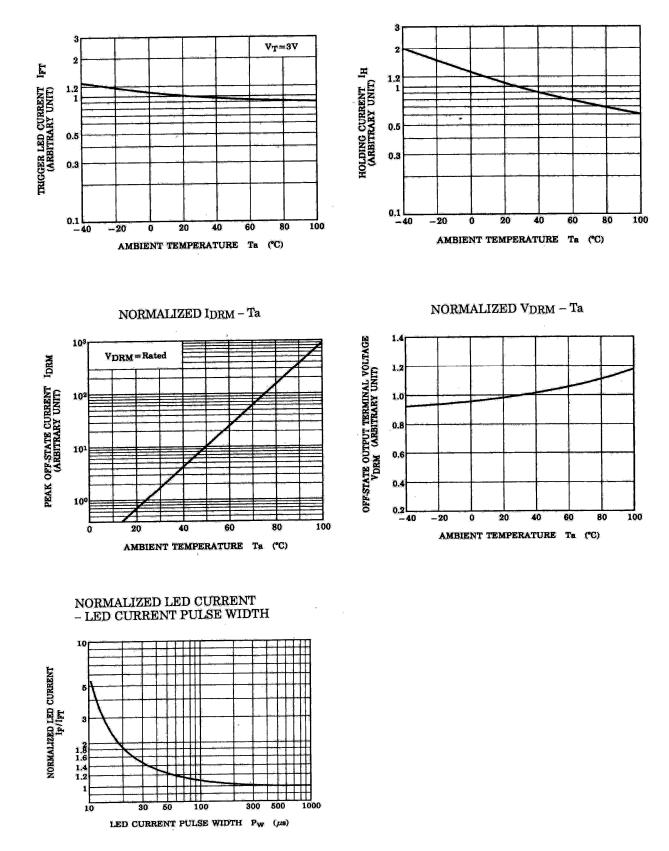


TOSHIBA



NORMALIZED IFT - Ta

NORMALIZED IH - Ta



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20070701-EN

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